

29. A method of manufacturing a high frequency high power device or a circuit including a high frequency high power device, the method including the steps of improving the
5 design of a similar existing device or of an existing circuit including such a device by performing the method of claim 28 and then manufacturing the device or the circuit including the device in accordance with the improved design.

10 30. An analyser for measuring at frequencies within a frequency range the response of an electronic device to a high frequency input signal, the analyser including:
an active load pull circuit connectable in use to a device to be analysed, the active load pull circuit
15 including
a feedback circuit arranged
(i) to receive an output signal from the device to be analysed,
(ii) to downconvert the signal received to a low frequency signal, to modify the low frequency signal, to upconvert the modified low frequency signal to a modified high frequency signal and
(iii) to feed the modified signal back to the device to be analysed, wherein
20 the feedback circuit is arranged to limit the magnitude gain of the feedback circuit at all frequencies within the frequency range.

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31. An analyser for measuring the response of an electronic device to a high frequency input signal, the analyser including:

5 an active load pull circuit connectable in use to a device to be analysed, the active load pull circuit including

10 a feedback circuit arranged (i) to receive an output signal from the device to be analysed, (ii) to modify the signal, the modification including limiting the magnitude gain of the feedback circuit at frequencies outside a band of frequencies, (iii) to feed the modified signal back to the device to be analysed, the modified signal fed back comprising a component having a frequency within said band,

15 characterised in that

the feedback circuit is also arranged to limit the magnitude gain of the feedback circuit at frequencies inside the band of frequencies.

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